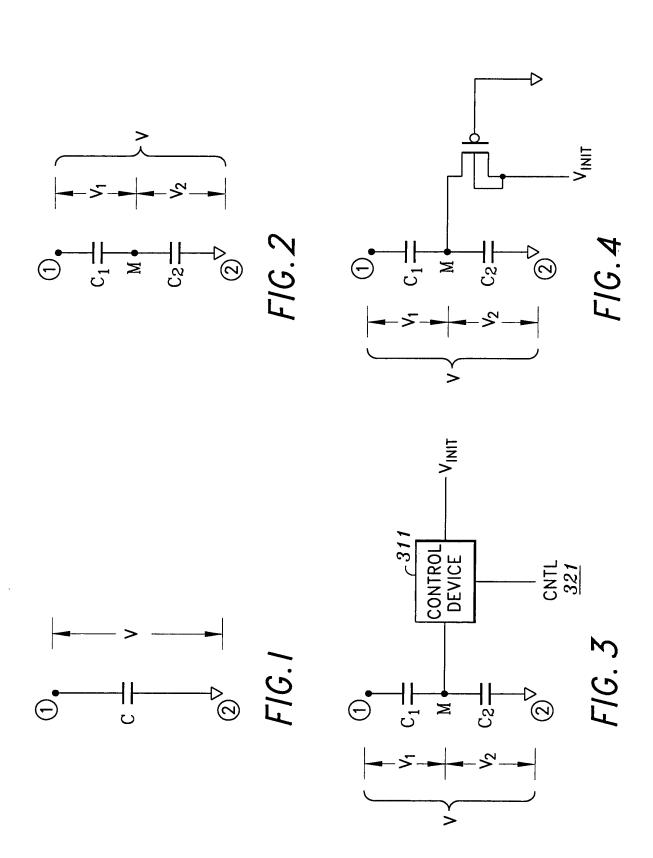
Blakely, Sokoloff, Taylor & Zafman LLP (714) 557-38
Title: METHOD AND APPARATUS FOR REDUCING STRESS ACROSS (714) 557-3800 CAPACITORS USED IN INTEGRATED CIRCUITS
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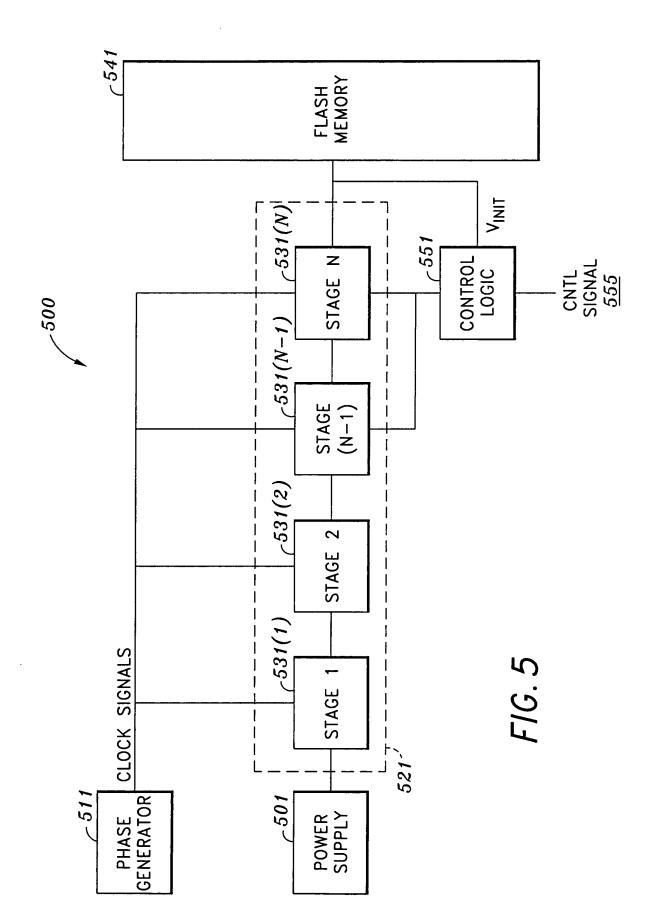
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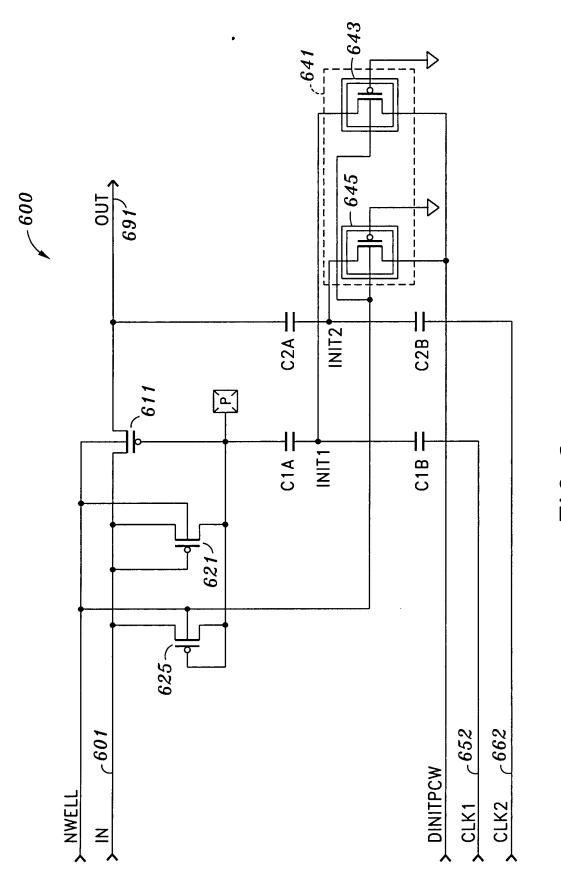
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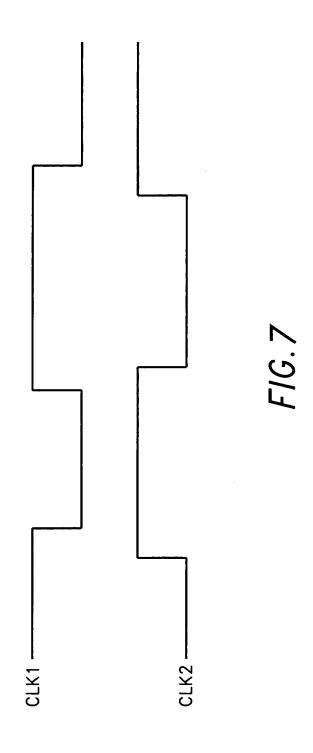
1st Named Inventor: Ramkarthik Ganesan Express Mail No.: EV323393856US Sheet: 3 of 7



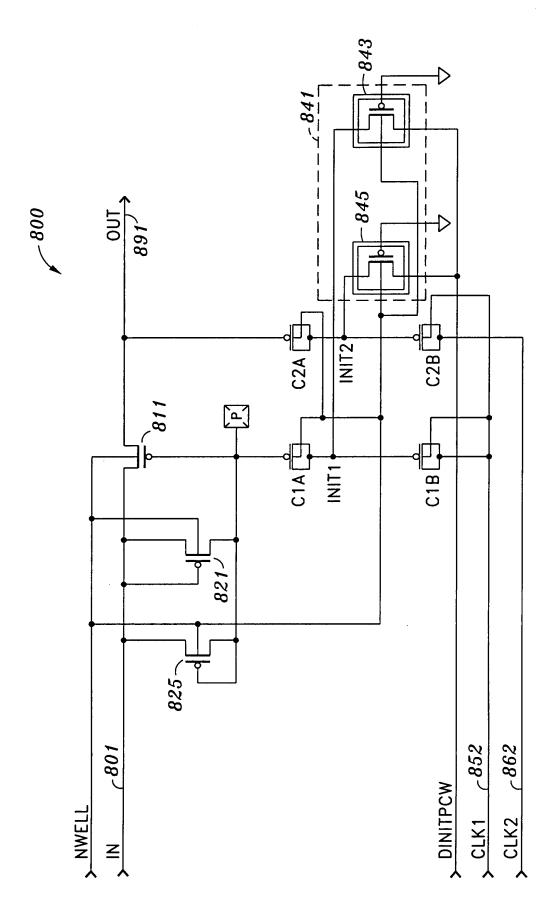
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Docket No.: 42P6963R

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1st Named Inventor: Ramkarthik Ganesan
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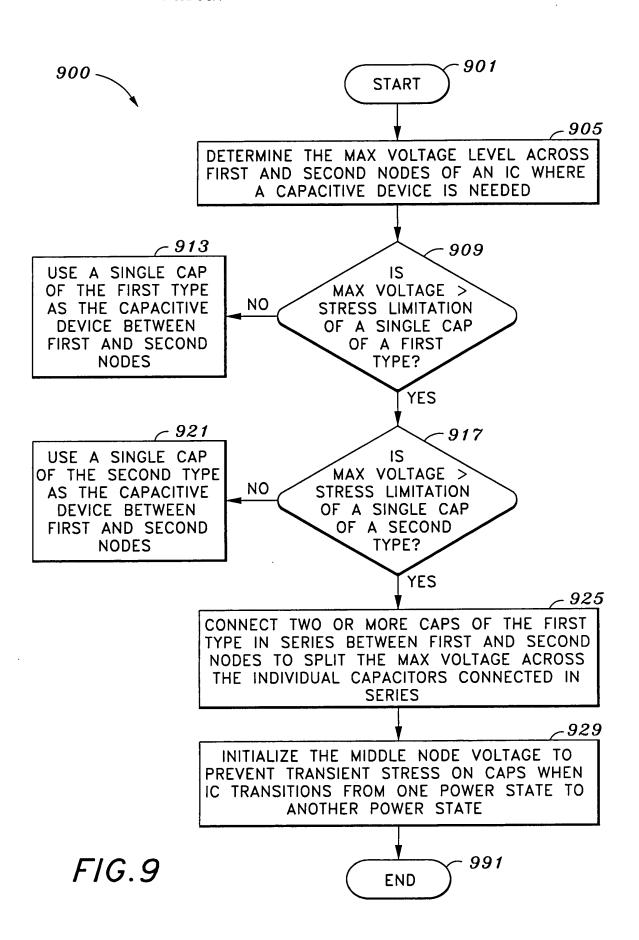
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